

Docket No.: GR 98 P 1507 P

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231, on the date indicated below.

By: [Signature]

Date: June 25, 2002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Andreas Rusch et al.  
Applic. No. : 09/685,361  
Filed : October 10, 2000  
Title : Semiconductor Memory Device and Method for Fabricating It  
Examiner : Lynne Gurley- Art Unit: 2812

INFORMATION DISCLOSURE STATEMENT  
UNDER 37 C.F.R. 1.97(C)(2)

Hon. Commissioner of Patents and Trademarks,  
Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

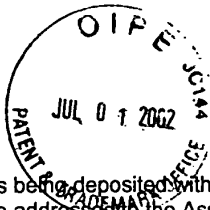
United States Patent No. 5,621,247 (Hirao et al.), dated April 15, 1997;

German Published, Non-Prosecuted Patent Application DE 40 08 883 A1 (Kaneko et al.), dated September 27, 1990, production method for a mask ROM and Mask ROM produced therewith;

Japanese Patent Abstract JP 59 067 666 A (Hanamura), dated April 17, 1984;

International Search Report dated September 29, 1999.

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications,



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GRL

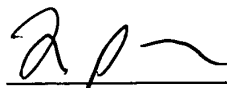
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patents and/or publications, it is because no existing translation is readily available to the applicant.

In accordance with 37 C.F.R. 1.97 (c) (2), consideration of this Information Disclosure Statement is requested.

Enclosed is the fee in the amount of \$180.00.

Respectfully submitted,



For Applicants

Mark P. Weichselbaum  
Reg. No. 43,248

Date: June 25, 2002

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